

This listing of claims replaces all prior versions and listings of claims in the application.

In the Claims:

1. (currently amended) A system for detecting a fault in a transmission link, comprising:

a reference level generator operable to output a reference level selectable according to one of a direct current (DC) mode threshold and an alternating current (AC) mode threshold, wherein the DC mode threshold is a fixed potential and the AC mode threshold varies with time; and

a comparator operable to detect the crossing of the reference level by an input signal arriving from the transmission link, the comparator having a degree of hysteresis selectable in response to a signal input to the comparator.
2. (original) A system as claimed in claim 1, wherein the AC mode threshold varies as a version of the input signal decaying with time.
3. (original) A system as claimed in claim 1, wherein said reference level generator is operable to selectably switch between outputting a lower threshold and an upper threshold as the DC mode threshold, wherein said comparator is operable to detect a crossing of the upper threshold and to detect a crossing of the lower threshold.
4. (original) A system as claimed in claim 3, wherein said comparator detects a

falling crossing of the lower threshold and detects a rising crossing of the upper threshold.

5. (original) A system as claimed in claim 4, wherein said reference level generator further comprises a multiplexer operable to select between outputting the DC mode threshold and the AC mode threshold.

6. (original) A system as claimed in claim 1, wherein said reference level generator includes a low-pass filter coupled to the input signal and is operable to generate the AC mode threshold from the input signal.

7. (currently amended) A system as claimed in claim 6, wherein said low-pass filter includes a series resistor coupled between the input signal and said comparator and a shunt capacitor coupled between the reference level and a fixed potential.

8. (cancelled)

9. (currently amended) A system as claimed in claim 78, wherein said low-pass filter includes a field effect transistor (FET) having a drain to which the input signal is coupled, said FET having a controlled transconductance.

10. (cancelled)

11. (original) A system as claimed in claim 1, wherein said system is operable to detect a short-circuited capacitor in an AC coupled transmission link when said comparator fails to detect a crossing of the DC mode threshold by the input signal.

12. (original) A system as claimed in claim 1, wherein said reference level generator is operable to output a first level of the AC mode threshold, wherein said comparator is operable to detect a falling crossing of the first level and said reference level generator is operable to output a second level of the AC mode threshold higher than the first level, wherein said comparator is operable to detect a rising crossing of the second level.

13. (original) A system as claimed in claim 12, wherein the separation between the first level and the second level is adjustable according to a setting of said comparator.

14. (original) A system as claimed in claim 12 wherein said reference level generator is further operable to vary the reference level between the first level and the second level in response to feedback from said comparator.

15. (original) A system as claimed in claim 14, wherein the reference level is set to the first level when said comparator detects a rising crossing of the reference level, and the

reference level is set to the second level when said comparator detects a falling crossing of the reference level.

16. (original) A system as claimed in claim 1, wherein said comparator is operable to detect a falling crossing of the reference level when the input signal reaches a first value and is operable to detect a rising crossing of the reference level when the input signal reaches a second value, wherein the separation between the first value and the second value is adjustable according to a setting of said comparator.

17. (original) A system as claimed in claim 1, wherein said reference level generator is operable to maintain the DC mode threshold at a substantially constant level, wherein said comparator is operable to detect a falling crossing of the DC mode threshold and to detect a rising crossing of the DC mode threshold at substantially the same level of the input signal.

18. (currently amended) A method of detecting a fault in a transmission link, comprising:

providing a reference level selectable according to one of a direct current (DC) mode threshold and an alternating current (AC) mode threshold, wherein the DC mode threshold is a fixed potential and the AC mode threshold varies with time; and

comparing an input signal arriving from the transmission link with one of the DC mode threshold and the AC mode threshold to determine whether a fault is present in the transmission link, said comparing being performed with different degrees of hysteresis depending upon a value of second signal.

19. (original) A method as claimed in claim 18, wherein the AC mode threshold varies as a version of the input signal decaying with time.

20. (original) A method as claimed in claim 19, wherein, when the input signal is compared to the DC mode threshold, the input signal is compared to each of a lower threshold and an upper threshold as the DC mode threshold.

21. (original) A method as claimed in claim 18, wherein the AC mode threshold is generated by low-pass filtering the input signal.

22. (original) A method as claimed in claim 18 further comprising detecting a short-circuited capacitor in an AC coupled transmission link when said comparing fails to detect a crossing of the DC mode threshold by the input signal.

23. (original) A method as claimed in claim 18, wherein the AC mode threshold is switched between a first level and a second level higher than the first level, wherein said

comparing detects a falling crossing of the first level and said comparing detects a rising crossing of the second level.

24. (original) A method as claimed in claim 23, wherein the separation between the first level and the second level is adjustable.

25-30. (cancelled)

31. (new) The method as claimed in claim 18, when said comparing is performed with a greater degree of hysteresis in an AC testing mode when the input signal is AC coupled to the transmission link than when the input signal is DC coupled to the transmission link

32. (new) The system as claimed in claim 1, wherein the comparator has a first degree of hysteresis during a DC mode when the system is DC coupled to the transmission link and has a second degree of hysteresis greater than the first degree during an AC mode when the system is AC coupled to the transmission link.

33. (new) The system as claimed in claim 1, wherein the degree of hysteresis is selectable in response to a second signal input to the comparator other than the input signal arriving from the transmission link.

34. (new) The system as claimed in claim 33, wherein the second signal is represented by multiple digital bits, the system further comprising a digital-to-analog converter operable to convert the second signal to an analog second signal having multiple different levels and the comparator is operable with different degrees of hysteresis in accordance with the multiple different levels of the analog second signal.

35. (new) A system for detecting a fault in a transmission link, comprising:

a reference level generator operable to output a reference level selectable according to one of a direct current (DC) mode threshold and an alternating current (AC) mode threshold, wherein the DC mode threshold is a fixed potential and the AC mode threshold varies with time, the reference level generator including a multiplexer for selecting between a DC mode threshold and an AC mode threshold, the multiplexer having substantial resistance and being operable together with a shunt capacitor to low-pass filter an input signal arriving from the transmission link to produce the AC mode threshold; and

a comparator operable to detect the crossing of the reference level by the input signal.